



Central Administration for General Education  
Educational Computer Material Development Department

Under the patronage of His Excellency the Minister of Education,

**Mr. Mohamed Abdel Latif**

Weekly Assessments for Programming and Artificial Intelligence for General  
Education (First Secondary Grade)

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## **Weekly Assessment:**

### **First Test**

**Choose the correct answer:**

1. What is the output of a NOT gate if the input is = 1?
  - A. 0
  - B. 1
  - C. 2
  - D. None
  
2. In the Truth Table for an OR gate, when the inputs are (0 and 0), the output is:
  - A. 0
  - B. 1
  - C. Depends on the gate
  - D. Undefined
  
3. Which of the following gates outputs (1) only when all inputs are (1)?
  - A. OR
  - B. AND
  - C. NOT
  - D. XOR
  
4. Which of the following best describes the Inverter (NOT) circuit?
  - A. It outputs the same value as the input
  - B. It outputs the inverse of the input value
  - C. It outputs the sum of the inputs
  - D. It does not output any value

## Second Test

**Choose the correct answer:**

**1. Which of the following describes the function of an OR gate?**

- A. It outputs 1 only when all inputs are 1**
- B. It outputs 1 if there is at least one input that is 1**
- C. It always outputs 0**
- D. It outputs 1 when the inputs are equal**

**2. The Truth Table is used for:**

- A. Data storage**
- B. Explaining the outputs for all possible input cases**
- C. Drawing circuits**
- D. Program translation**

**3. The Full Adder circuit deals with:**

- A. One bit only**
- B. Two bits and three inputs**
- C. Two bits and two inputs only**
- D. Four inputs**

**4. When adding 1 and 1 in the binary system, the result is:**

- A. 0**
- B. 1**
- C. 10**
- D. 11**

### **Third Test**

**Choose the correct answer:**

**1. The function of the Half Adder circuit is to calculate:**

- A. The Sum only**
- B. The Sum and Carry together**
- C. Subtraction**
- D. Division**

**2. What is the difference between a Half Adder circuit and a Full Adder circuit?**

- A. The Full Adder takes the Carry into consideration**
- B. The Half Adder is faster in calculation**
- C. The Full Adder uses only one input**
- D. The Half Adder only works in logical operations**

**3. In the Full Adder circuit, the number of inputs is:**

- A. 2**
- B. 3**
- C. 4**
- D. 1**

**4. In logic circuits, the value (1) represents:**

- A. False**
- B. True**
- C. Null**
- D. Off**

