

Central Administration for General Education

Educational Computer Material Development Department

Under the patronage of His Excellency the Minister of Education,

Mr. Mohamed Abdel Latif

Weekly Assessments for Programming and Artificial Intelligence for General Education (First Secondary Grade)

First Semester of the 2025/2026 Academic Year

**Tenth Eleventh Week** 

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## **First Test**

<ul> <li>a. 0</li> <li>b. 1</li> <li>c. 2</li> <li>d. Nothing</li> </ul> 2. In the truth table for an OR gate, when the inputs are (0 and 0), the output is: <ul> <li>a. 0</li> <li>b. 1</li> <li>c. Depends on the gate</li> <li>d. Undefined</li> </ul>
<ul> <li>c. 2</li> <li>d. Nothing</li> </ul> 2. In the truth table for an OR gate, when the inputs are (0 and 0), the output is: <ul> <li>a. 0</li> <li>b. 1</li> <li>c. Depends on the gate</li> </ul>
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a. 0 b. 1 c. Depends on the gate
b. 1 c. Depends on the gate
c. Depends on the gate
d. Undefined
3. What is the logical symbol for an AND gate?
a. V
<b>b.</b> Λ
C. ¬
d. +
4. In a Full Adder circuit, the symbol C refers to:
a. Carry
b. Sum
c. A third input
d. Power line

## **Second Test**

- 1. The function of a truth table is to:
  - a. Show all possible input and output combinations
  - b. Save programming commands
  - c. Analyze images
  - d. Display only binary values
- 2. The circuit that outputs the opposite of the input is called:
  - a. OR
  - b. NOT
  - c. AND
  - d. XOR
- 3. A Full Adder circuit handles:
  - a. Only one bit
  - b. Two bits and three inputs
  - c. Two bits and only two inputs
  - d. Four inputs
- 4. When adding 1 and 1 in the binary system, the result is:
  - a. 0
  - b. 1
  - c. 10
  - **d. 11**

## **Third Test**

L. The function of a Half Adder circuit is to calculate:	
a. The Sum only	
b. Both the Sum and the Carry	
c. Subtraction	
d. Division	
2. The logic circuit that outputs 1 only when the inputs are different is called	d:
a. XOR	
b. OR	
c. AND	
d. NOR	
3. Which of the following describes a Full Adder?	
a. It includes only two inputs	
b. It includes 3 inputs and produces two outputs (Sum and Carry)	
c. It produces only one output	
d. It does not contain an XOR gate	
I. In logic circuits, the value (1) means:	
a. False	
b. True	
c. Null	
d. Off	