



Central Administration for General Education
Educational Computer Material Development Department

Under the patronage of His Excellency the Minister of Education,

Mr. Mohamed Abdel Latif

Weekly Assessments for Programming and Artificial Intelligence for General
Education (First Secondary Grade)

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Tenth Week

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Weekly Assessment:

First Test

- 1. What is meant by a Logical Operation?**
 - a. A calculation using decimal numbers**
 - b. An operation performed using binary numbers 0 and 1**
 - c. A graphical process for drawing circuits**
 - d. A process of storing data in memory**

- 2. A Logic Circuit is used for:**
 - a. Converting signals into images**
 - b. Executing logical operations**
 - c. Storing data**
 - d. Sending data over the internet**

- 3. Which of the following gates outputs (1) only when all inputs are (1)?**
 - a. OR**
 - b. AND**
 - c. NOT**
 - d. XOR**

- 4. Which of the following describes the Inverter (NOT) circuit?**
 - a. It outputs the same value as the input**
 - b. It outputs the opposite of the input value**
 - c. It outputs the sum of the inputs**
 - d. It does not output any value**

Second Test

1. Which of the following describes the function of an OR gate?

- a. It outputs 1 only when all inputs are 1**
- b. It outputs 1 if at least one input is 1**
- c. It always outputs 0**
- d. It outputs 1 when the inputs are equal**

2. A Truth Table is used for:

- a. Storing data**
- b. Showing the outputs for all possible input combinations**
- c. Drawing circuits**
- d. Translating programs**

3. What is the output of an AND gate if the inputs are (1 and 0)?

- a. 0**
- b. 1**
- c. 2**
- d. Nothing**

4. What is the function of a Half Adder circuit?

- a. Adding two decimal numbers**
- b. Adding two single-bit binary numbers**
- c. Subtracting two numbers**
- d. Converting one number to another**

Third Test

- 1. A Half Adder circuit consists of:**
 - a. An AND gate and an OR gate**
 - b. An AND gate and a NOT gate**
 - c. An AND gate and an XOR gate**
 - d. An OR gate and a NOT gate**

- 2. What is the difference between a Half Adder and a Full Adder?**
 - a. The Full Adder accounts for the Carry-in**
 - b. The Half Adder is faster at calculation**
 - c. The Full Adder uses only one input**
 - d. The Half Adder only works for logical operations**

- 3. In a Full Adder circuit, the number of inputs is:**
 - a. 2**
 - b. 3**
 - c. 4**
 - d. 1**

- 4. Which of the following is NOT a component of a Full Adder circuit?**
 - a. AND**
 - b. OR**
 - c. XOR**
 - d. NAND**

Answer Key

Question No.	Test 1	Test 2	Test 3
1	b	b	c
2	b	b	a
3	b	a	b
4	b	b	d